

**Amendments to the Specification:**

After the title, please insert the following subheading and paragraph:

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application is entitled to the benefit of and incorporates by reference essential subject matter disclosed in International Patent Application No. PCT/DK2003/000422 filed on June 20, 2003 and Danish Patent Application No. PA 2002 00966 filed on June 23, 2002.

Before paragraph [0002], please insert the following subheading:

FIELD OF THE INVENTION

Before paragraph [0003], please insert the following subheading:

BACKGROUND OF THE INVENTION

Before paragraph [0004], please insert the following subheading:

SUMMARY OF THE INVENTION

Before paragraph [0030], please insert the following subheading.

BRIEF DESCRIPTION OF THE DRAWINGS

Please amend paragraphs [0030] to [0042] as follows:

[0031] Figure 1 shows paralleled DC/DC modules and one DC/AC module according to the invention connected to a common DC-bus.

[0032] Figure 2 shows a modularized power converter according to the invention with an H-bridge DC/DC-converter.

[0033] Figure 3 is a diagram according to the invention of a grid connected DC/AC inverter with a full bridge DC/DC-converter.

[0034] Figure 4 shows the waveforms of the circuit in Fig.3.

[0035] Figure 5 is an elaboration of Figure 3, now incorporating an active clamp in the DC/DC module.

[0036] Figure 6 shows the waveforms of the circuit in Fig.5.

[0037] Figure 7 is a diagram of a full bridge DC/DC-converter with an active snubber circuit.

[0038] Figure 8 is a diagram according to the invention of a grid connected inverter with a push-pull DC/DC-converter.

[0039] Figure 9 shows the transfer characteristic of an analog PWM generator used in the push-pull DC/DC-converter.

[0040] Figure 10 shows the relation between duty cycle and current and voltage mode in the push-pull converter.

[0041] Figure 11 illustrates the pulse sequence generated by a microprocessor in voltage mode of the push pull converter.

[0042] Figure 12 shows the ideal characteristic of a load for a snubber circuit and its practical implementation for use in a push pull converter.

Before paragraph [0043], please insert the following subheading:

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Please add the following new paragraph [00100].

[00100] While the present invention has been illustrated and described with respect to a particular embodiment thereof, it should be appreciated by those of ordinary skill in the art that various modifications to this invention may be made without departing from the spirit and scope of the present invention.